FIG. 1

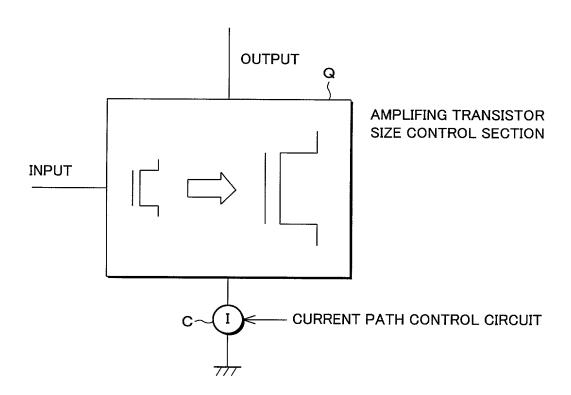


FIG. 2

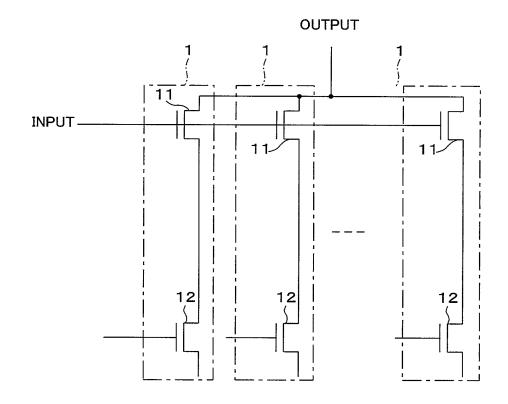


FIG. 3(a)

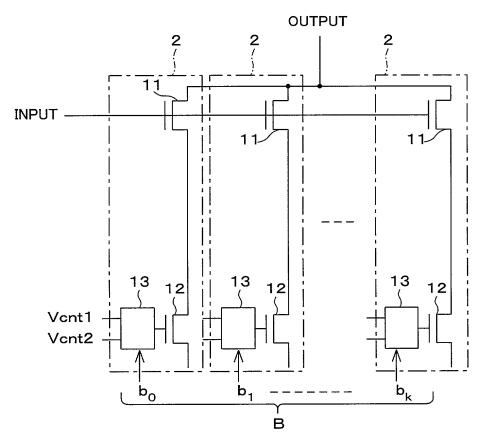


FIG. 3(b)

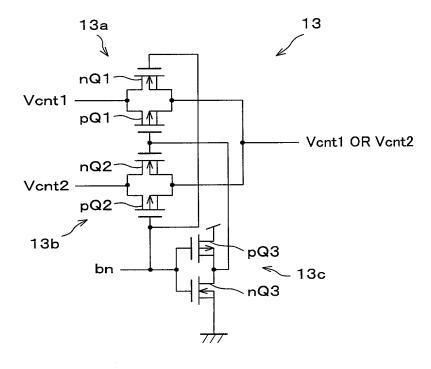
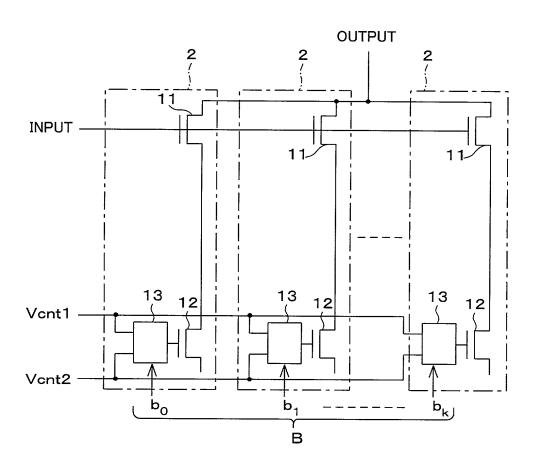
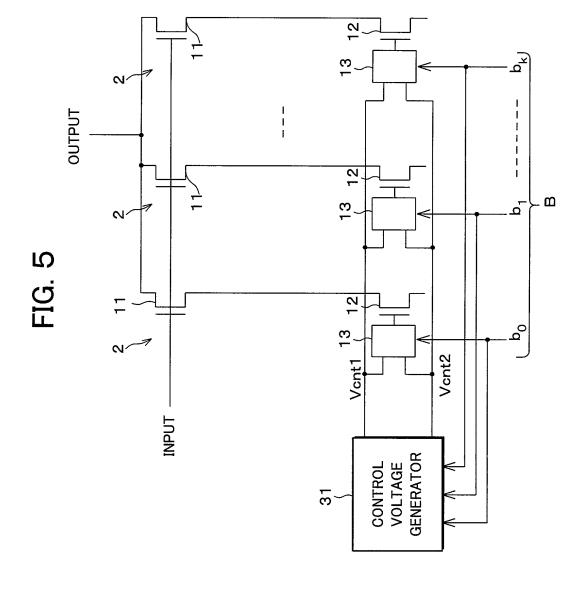
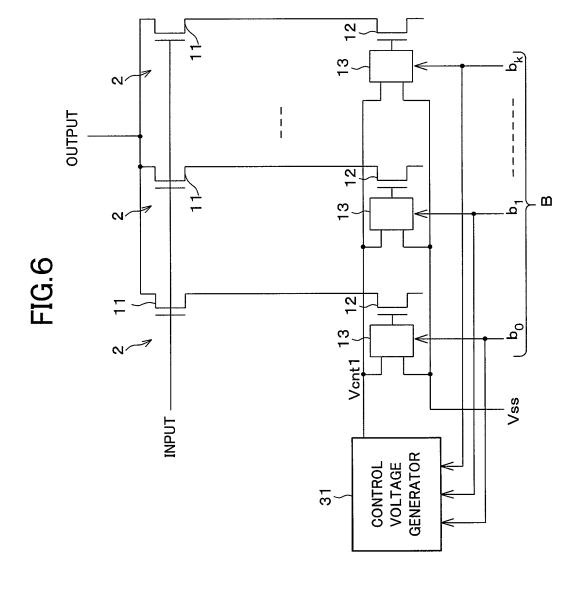


FIG.4







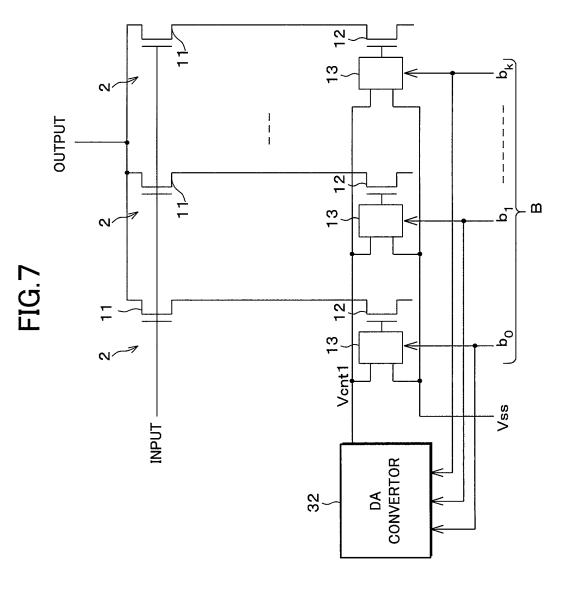


FIG.8

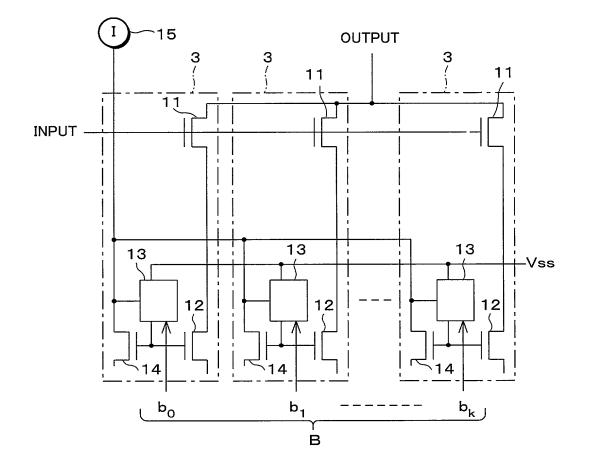


FIG. 9(a)

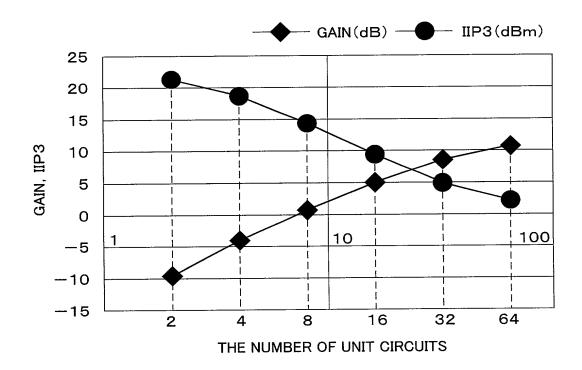
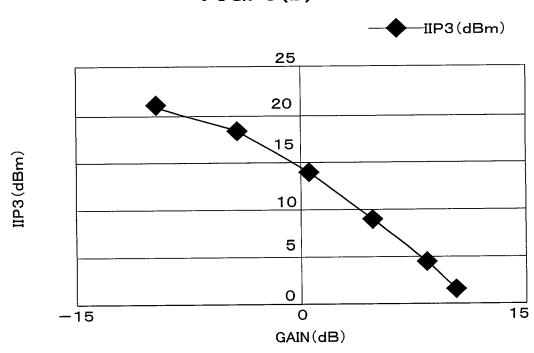
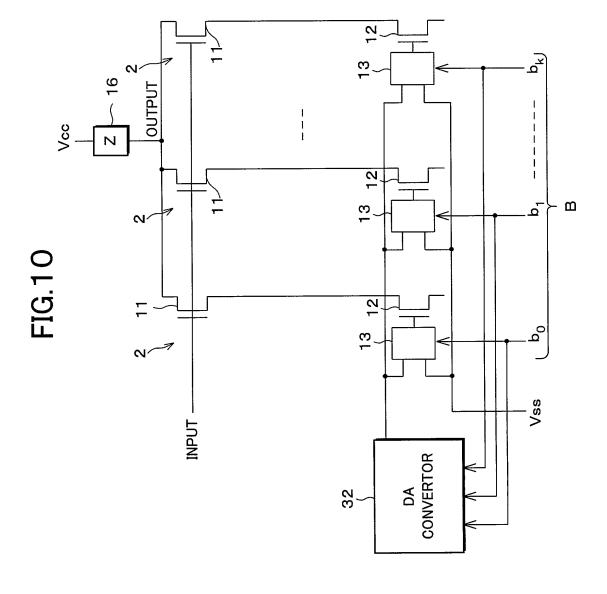


FIG. 9(b)





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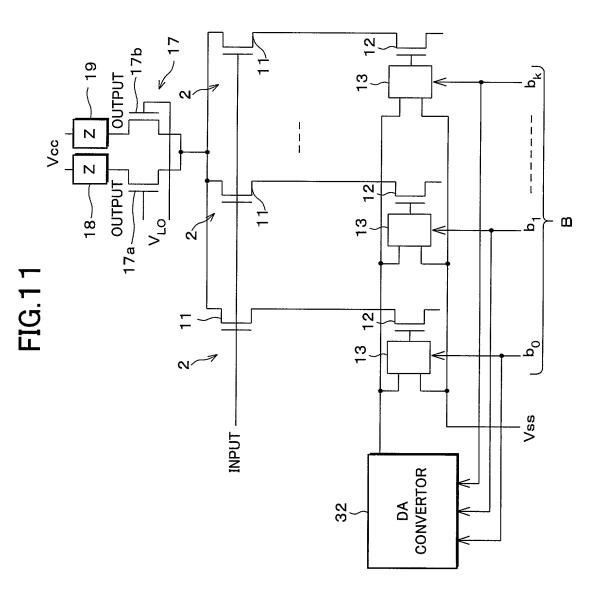


FIG.12

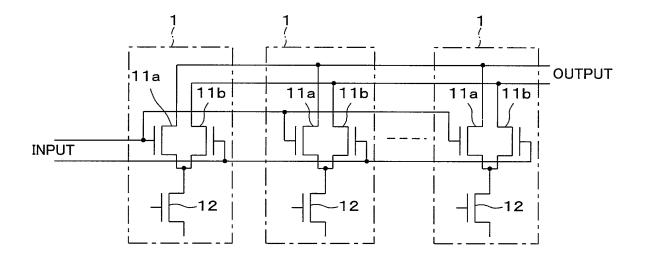


FIG.13

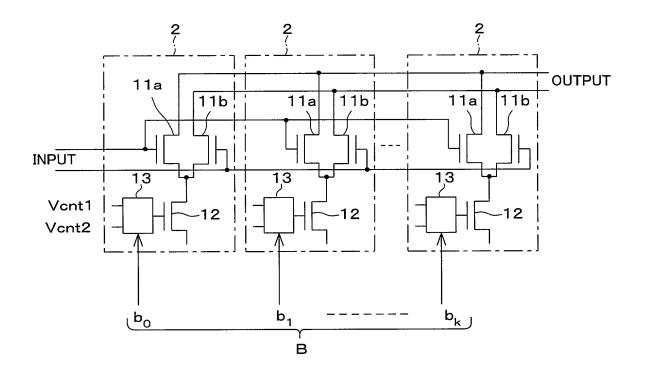


FIG.14

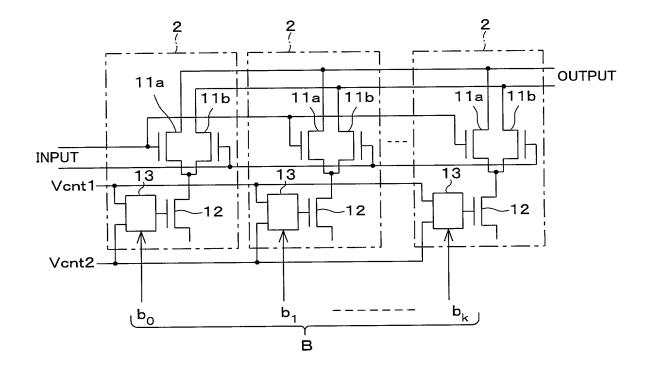


FIG.15

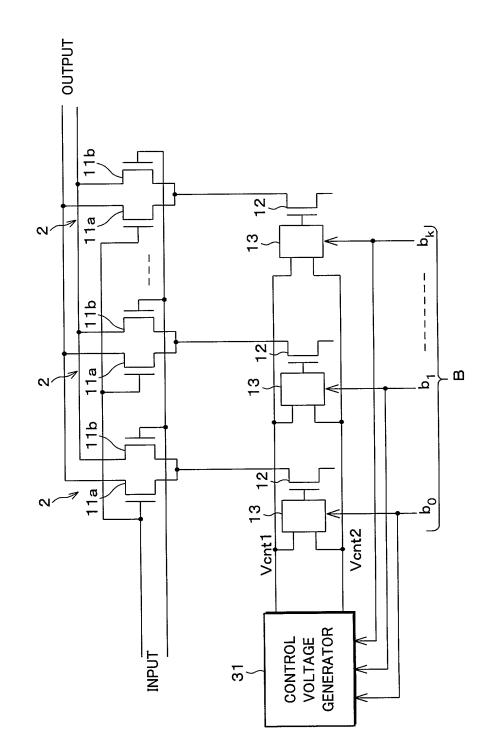


FIG.16

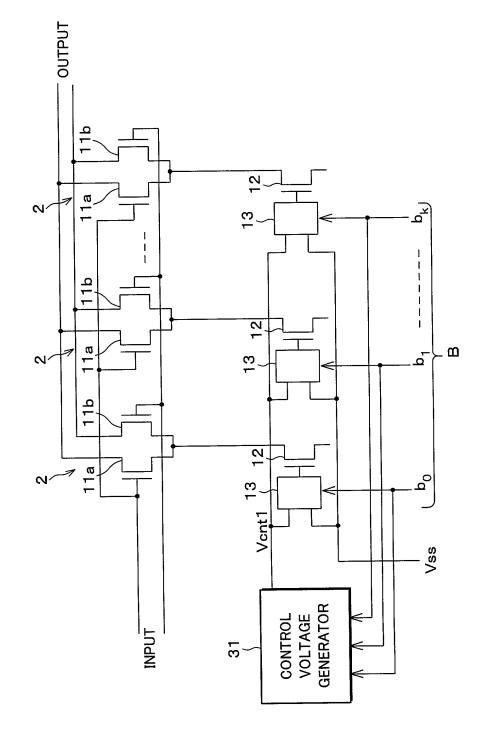


FIG.17

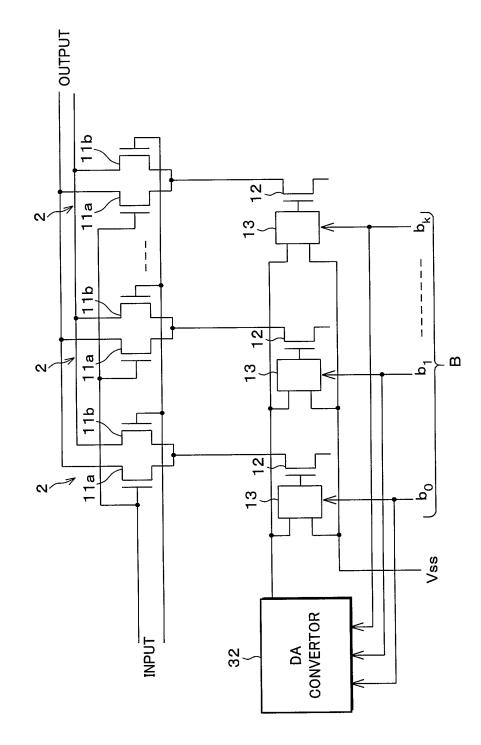
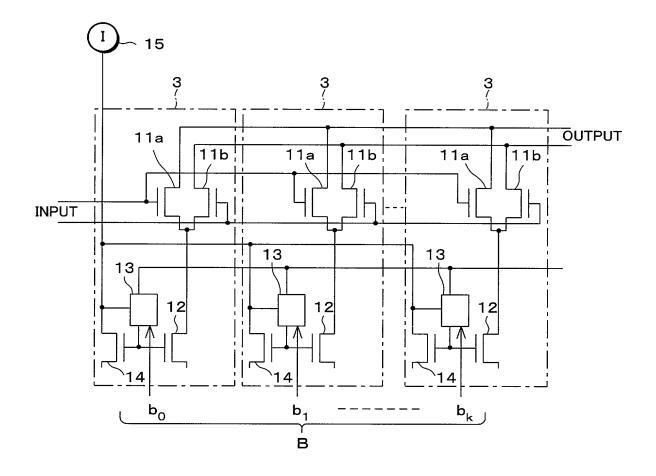
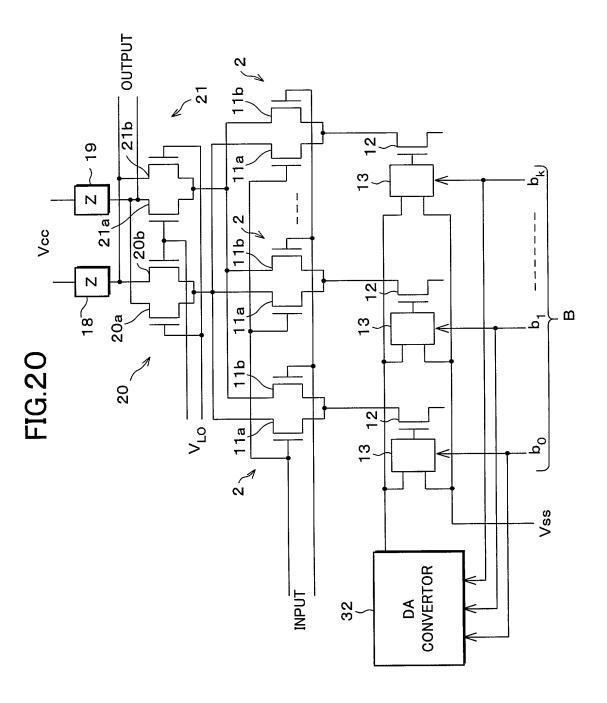


FIG.18



OUTPUT ∾′ ~ 19 Ν 11b \ \ \ Ν 1,2 <del>1</del>8 13 0/ FIG. 19 72 1<u>1</u>a اه ا 13 Vss DA CONVERTOR INPUT 35

i i



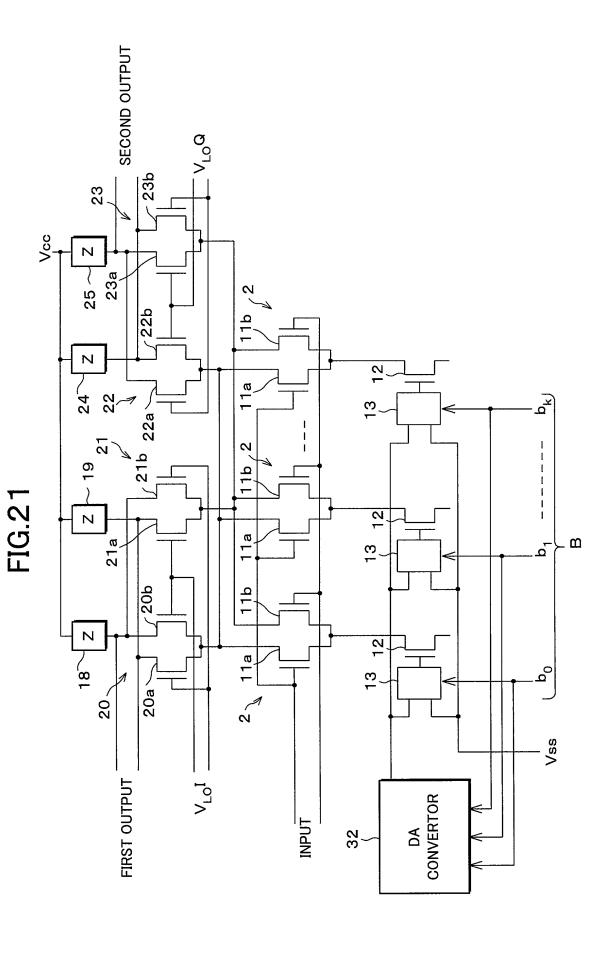


FIG. 22(a)

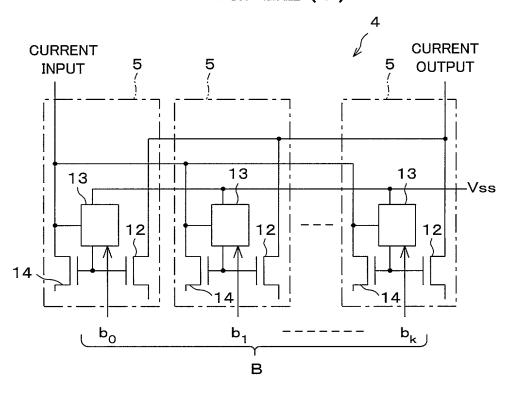


FIG. 22(b)

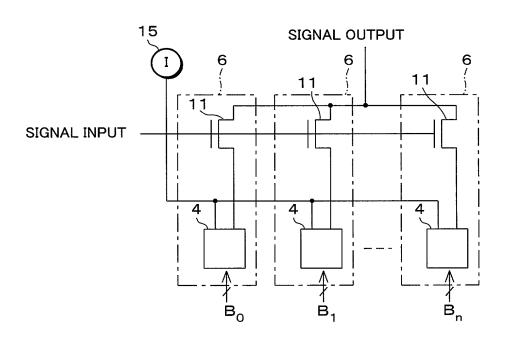


FIG. 23

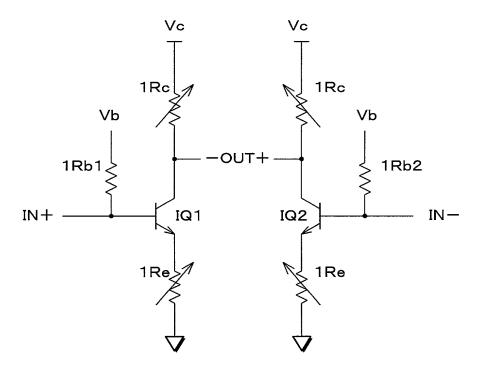


FIG. 24

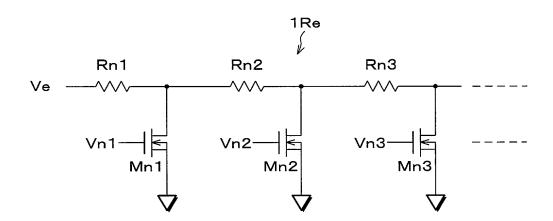
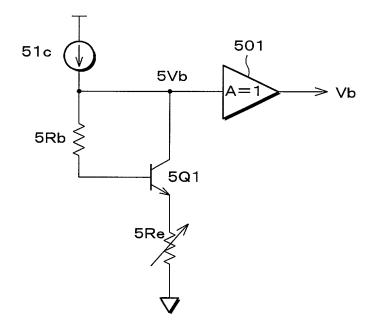


FIG. 25



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FIG. 26

